



This is to certify that

Pedro Sibaja Terán

has successfully completed

FPGA - Xilinx. - Spartan 6

Advanced digital design methodology

Mexico City, December 7-10, 2010


Edgard Garcia
Instructor



MULTI VIDEO DESIGNS
FPGA Experts!


Guillermina Avendaño
Microsystems Program Coordinator



Fundación México-Estados Unidos para la Ciencia
The United States-Mexico Foundation for Science