



This is to certify that

*Luis Manuel López Pozos*

has successfully completed

*FPGA - Xilinx. - Spartan 6*

*Advanced digital design methodology*

Mexico City, December 7-10, 2010

Edgard Garcia  
Instructor



**MULTI VIDEO DESIGNS**  
*FPGA Experts!*

Guillermina Avendaño  
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