XILINX®
Customer Education

This is to certify that

José Antonio Arellano Mejía

has successfully completed

FPGA - Xilinx.- Spartan 6
Advanced digital design methodology

Mexico City, December 7-10, 2010

Edgard Garcia
Instructor

MVD
FPGA
Experts!

MULTI VIDEO DESIGNS
FPGA Experts !

Guillermina Avendaño
Microsystems Program Coordinator

FUMEC
Fundación México-Estados Unidos para la Ciencia
The United States-Mexico Foundation for Science