



This is to certify that

Jorge Valeriano Assem

has successfully completed

FPGA - Xilinx. - Spartan 6

Advanced digital design methodology

Mexico City, December 7-10, 2010

Edgard Garcia
Instructor



MULTI VIDEO DESIGNS
FPGA Experts!

Guillermina Avendaño
Microsystems Program Coordinator

A handwritten signature in blue ink, appearing to read 'Guillermina Avendaño'.

